Day : Thursday
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Inventor Name Search Result

Your Search was:

Last Name = RASHID
First Name = ABBAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09377503	6717576	150	08/20/1999	DEFERRED SHADINGGRAPHICS PIPELINE PROCESSOR HAVING ADVANCED FEATURES	RASHID, ABBAS
09378408	6288730	150	08/20/1999	METHOD AND APPARATUS FOR GENERATING TEXTURE	RASHID, ABBAS
09724663	Not Issued	71	11/28/2000	Method and apparatus for generating texture	RASHID, ABBAS
09900514	Not Issued	80	07/06/2001	Cross-bar switch	RASHID, ABBAS
10036595	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	RASHID, ABBAS
10036602	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	RASHID, ABBAS
10036603	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	RASHID, ABBAS
10036622	Not Issued	41	12/21/2001	Cross-bar switch employing a multiple entry point FIFO	RASHID, ABBAS
10036762	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	RASHID, ABBAS
10037144	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	RASHID, ABBAS
10191742	Not Issued	41	07/08/2002	Content service aggregation system	RASHID, ABBAS
10458493	Not Issued	41	06/09/2003	Deferred shading graphics pipeline processor having advanced features	RASHID, ABBAS
10682579	Not Issued	41	10/08/2003	Advanced telecommunications processor	RASHID, ABBAS
10898007	Not	30	07/23/2004	Advanced processor scheduling	RASHID, ABBAS

	Issued			in a multithreaded system	
10898008	Not Issued	41	07/23/2004	Advanced processor with system on a chip interconnect technology	RASHID, ABBAS
10930175	Not Issued	20	08/31/2004	Advanced processor with a thread aware return address stack optimally used across active threads	RASHID, ABBAS
10930186	Not Issued	20	08/31/2004	Advanced processor with scheme for optimal packet flow in a multi-processor system on a chip	RASHID, ABBAS
10930455	Not Issued	30	08/31/2004	Advanced processor with mechanism for fast packet queuing operations	RASHID, ABBAS
10930937	Not Issued	30	08/31/2004	Advanced processor with interfacing messaging network to a CPU	RASHID, ABBAS
10930939	Not Issued	30	08/31/2004	Advanced processor with mechanism for maximizing resource usage in an in-order pipeline with multiple threads	RASHID, ABBAS
10931003	Not Issued	20	08/31/2004	Advanced processor with interrupt delivery mechanism for multi-threaded multi-CPU system on a chip	RASHID, ABBAS
60303354	Not Issued	159	07/06/2001	Content service aggregation system	RASHID, ABBAS
07835030	Not Issued	161	02/10/1992	METHOD FOR INTERFERON PRODUCTION FROM HUMAN PERIPHERAL BLOOD LEUKOCYTES AND ALPHA INTERFERON PRODUCED THEREBY	RASHIDBAIGI, ABBAS
08129089	5503828	150	10/05/1993	ALPHA INTERFERON COMPOSITION AND METHOD FOR ITS PRODUCTION FROM HUMAN PERIPHERAL BLOOD LEUKOCYTES	RASHIDBAIGI, ABBAS
08144601	Not Issued	164	10/27/1993	COMPOSITION CONTAINING HUMAN ALPHA INTERFERON SPECIES PROTEINS AND METHOD FOR USE THEREOF	RASHIDBAIGI, ABBAS
08454444	5676942	150	05/25/1995	COMPOSITION CONTAINING	RASHIDBAIGI,

				HUMAN ALPHA INTERFERON SPECIES PROTEINS AND METHOD FOR USE THEREOF	ABBAS
08996070	5989441	150	12/22/1997	RECOVERY OF FUNCTIONAL HUMAN LEUKOCYTES FROM RECYCLED FILTERS	RASHIDBAIGI, ABBAS

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = ZAIDI
First Name = NAZAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08780249	6016540	150	01/08/1997	METHOD AND APPARATUS FOR SCHEDULING INSTRUCTIONS IN WAVES	ZAIDI, NAZAR
08780255	6065105	150	01/08/1997	DEPENDENCY MATRIX	ZAIDI, NAZAR
08986008	6032250	150	12/05/1997	METHOD AND APPARATUS FOR IDENTIFYING INSTRUCTION BOUNDARIES	ZAIDI, NAZAR
09209094	6292882	150	12/10/1998	METHOD AND APPARATUS FOR FILTERING VALID INFORMATION FOR DOWNSTREAM PROCESSING	ZAIDI, NAZAR
09900481	6839808	150	07/06/2001	"PROCESSING CLUSTER HAVING MULTIPLE COMPUTE ENGINES AND SHARES TIER ONE CACHES"	ZAIDI, NAZAR
09900514	Not Issued	80	07/06/2001	Cross-bar switch	ZAIDI, NAZAR
10036595	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	ZAIDI, NAZAR
10036602	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	ZAIDI, NAZAR
10036603	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	ZAIDI, NAZAR
10036622	Not Issued	41	12/21/2001	Cross-bar switch employing a multiple entry point FIFO	ZAIDI, NAZAR
10036762	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	ZAIDI, NAZAR
10037144	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	ZAIDI, NAZAR
10105151	6745289	150	03/25/2002	PROCESSING PACKETS IN CACHE MEMORY	ZAIDI, NAZAR

10105924	6880049	150	03/25/2002	SHARING A SECOND TIER CACHE MEMORY IN A MULTI-PROCESSOR	ZAIDI, NAZAR
10105972	6895477	150	03/25/2002	RING-BASED MEMORY REQUESTS IN A SHARED MEMORY MULTI-PROCESSOR	ZAIDI, NAZAR
10105973	6898673	150	03/25/2002	CO-PROCESSOR INCLUDING A MEDIA ACCESS CONTROLLER	ZAIDI, NAZAR
10105979	6920542	150	03/25/2002	APPLICATION PROCESSING EMPLOYING A COPROCESSOR	ZAIDI, NAZAR
10105993	6892282	150	03/25/2002	RING BASED MULTI-PROCESSING SYSTEM	ZAIDI, NAZAR
10106925	6901482	150	03/25/2002	MANAGING OWNERSHIP OF A FULL CACHE LINE USING A STORE-CREATE OPERATION	ZAIDI, NAZAR
10191742	Not Issued	41	07/08/2002	Content service aggregation system	ZAIDI, NAZAR
10191746	Not Issued	30	07/08/2002	Content service aggregation system control architecture	ZAIDI, NAZAR
60120396	Not Issued	159	02/17/1999	METHOD OF DYNAMICALLY EXPANDING AND EXECUTING MICROCODE ROUTINES	ZAIDI, NAZAR
60303354	Not Issued	159	07/06/2001	Content service aggregation system	ZAIDI, NAZAR
09001251	5996064	150	12/30/1997	METHOD AND APPARATUS FOR GUARANTEEING MINIMUM VARIABLE SCHEDULE DISTANCE BY USING POST-READY LATENCY	ZAIDI, NAZAR A.
09520424	6574689	150	03/08/2000	METHOD AND APPARATUS FOR LIVE-LOCK PREVENTION	ZAIDI, NAZAR A.
10247894	Not Issued	41	09/19/2002	Processor utilizing novel architectural ordering scheme	ZAIDI, NAZAR A.
10682579	Not Issued	41	10/08/2003	Advanced telecommunications processor	ZAIDI, NAZAR A.
60416838	Not Issued	159	10/08/2002	Advanced telecommunications processor	ZAIDI, NAZAR A.
60490236	Not Issued	159	07/25/2003	Advanced telecommunications processor	ZAIDI, NAZAR A.
08672409	5944818	150	06/28/1996	METHOD & APPARATUS FOR ACCELERATED INSTRUCTION RESTART IN A MICROPROCESSOR	ZAIDI, NAZAR A.

08769068	5918031	150	12/18/1996	COMPUTER UTILIZING SPECIAL MICRO-OPERATIONS FOR ENCODING OF MULTIPLE VARIANT CODE FLOWS	ZAIDI, NAZAR A.
08779791	6049897	150	01/07/1997	MULTIPLE SEGMENT REGISTER USE WITH DIFFERENT OPERAND SIZE	ZAIDI, NAZAR A.
08994400	5954814	150	12/19/1997	SYSTEM FOR USING A BRANCH PREDICTION UNIT TO ACHIEVE SERIALIZATION BY FORCING A BRANCH MISPREDICTION TO FLUSH A PIPELINE	ZAIDI, NAZAR A.
09001170	5961630	150	12/30/1997	METHOD AND APPARATUS FOR HANDLING DYNAMIC STRUCTURAL HAZARDS AND EXCEPTIONS BY USING POST-READY LATENCY	ZAIDI, NAZAR A.
09001741	6216221	150	12/31/1997	METHOD AND APPARATUS FOR EXPANDING INSTRUCTIONS	ZAIDI, NAZAR A.
09002236	5961615	150	12/31/1997	METHOD AND APPARATUS FOR QUEUING DATA	ZAIDI, NAZAR A.
09002774	6044456	150	01/05/1998	SYSTEM AND METHOD TO MAINTAIN SYNCHRONIZATION OF DATA BETWEEN MULTIPLE PARALLEL FRONT-END PIPELINES	ZAIDI, NAZAR A.
09088634	6405307	150	06/02/1998	APPARATUS AND METHOD FOR DETECTING AND HANDLING SELF-MODIFYING CODE CONFLICTS IN AN INSTRUCTION FETCH PIPELINE	ZAIDI, NAZAR A.
09089736	6237088	150	06/03/1998	SYSTEM AND METHOD FOR TRACKING IN-FLIGHT INSTRUCTIONS IN A PIPELINE	ZAIDI, NAZAR A.
09183084	6363408	150	10/30/1998	METHOD AND APPARATUS FOR SUMMING SELECTED BITS FROM A PLURALITY OF MACHINE VECTORS	ZAIDI, NAZAR A.
09217774	6523106	150	12/21/1998	METHOD AND APPARATUS FOR EFFICIENT PIPELINING	ZAIDI, NAZAR A.
09314439	6055652	150	05/19/1999	MULTIPLE SEGMENT REGISTER USE WITH	ZAIDI, NAZAR A.

				DIFFERENT OPERAND SIZE	
09476660	6581154	150	12/31/1999	EXPANDING MICROCODE ASSOCIATED WITH FULL AND PARTIAL WIDTH MACROINSTRUCTIONS	ZAIDI, NAZAR A.
09505949	Not Issued	121	02/15/2000	Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support	ZAIDI, NAZAR ABBAS
09473577	6542981	150	12/28/1999	MICROCODE UPGRADE AND SPECIAL FUNCTION SUPPORT BY EXECUTING RISC INSTRUCTION TO INVOKE RESIDENT MICROCODE	ZAIDI, NAZAR ABBAS

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Inventor Name Search Result

Your Search was:

Last Name = BRYERS
First Name = MARK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07346648	5058110	150	05/03/1989	PROTOCOL PROCESSOR	BRYERS, MARK
09900514	Not Issued	80	07/06/2001	Cross-bar switch	BRYERS, MARK
10036595	Not Issued	94	12/21/2001	CROSS-BAR SWITCH WITH BANDWIDTH ALLOCATION	BRYERS, MARK
10036602	Not Issued	71	12/21/2001	Cross-bar switch with explicit multicast support	BRYERS, MARK
10036603	Not Issued	71	12/21/2001	Cross-bar switch supporting implicit multicast addressing	BRYERS, MARK
10036762	Not Issued	71	12/21/2001	Cross-bar switch incorporating a sink port with retry capability	BRYERS, MARK
10037144	Not Issued	41	12/21/2001	Cross-bar switch with sink port accepting multiple packets	BRYERS, MARK
10105508	6938093	150	03/25/2002	BANDWIDTH ALLOCATION FOR A DATA PATH	BRYERS, MARK
10191742	Not Issued	41	07/08/2002	Content service aggregation system	BRYERS, MARK
10191746	Not Issued	30	07/08/2002	Content service aggregation system control architecture	BRYERS, MARK
11189595	Not Issued	30	07/26/2005	Network device having a co-processor including a media access controller and streaming engine	BRYERS, MARK
60027339	Not Issued	159	10/01/1996	ROADRUNNER PCI/HIPPI CONTROLLER	BRYERS, MARK
60303354	Not Issued	159	07/06/2001	Content service aggregation system	BRYERS, MARK

Inventor Search Completed: No Records to Display.

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BRYERS MARK Search

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S1	352	(370/424 370/412-418, 370/428-429)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 08:51
S2	6	S1 and ((crossbar cross-bar) same multicast$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 14:59
S3	11	S1 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S4	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S5	1	S4 and pio	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 13:44
S6	4241	(370/424 370/412-418, 370/428-429).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/21 14:58
S7	49	S6 and ((crossbar cross-bar) same multicast$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:06
S8	352	(370/424 370/412-418, 370/428-429)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/21 14:59
S9	44	S7 not S8	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 14:59
S10	35	S6 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:06
S11	11	S8 and ((crossbar cross-bar) same ring)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:07
S12	27	S10 not S11	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:11

S13	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:43
S14	1	S13 and ("370" with "390")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:41
S15	823	ack with ready	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:42
S16	102	ack with ready with (transmit transmission)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/21 15:43
S17	565	(transmission adj1 request) near3 packet	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 08:54
S18	45	((transmission adj1 request) near3 packet) same (confirm$2 confirmation ack acknowldegement)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:28
S19	32657	(rts same cts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:28
S20	2556	(rts same cts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S21	0	(rts same cts) and (request adj1 to adj1 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S22	926	(rts same cts) and (request adj2 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:29
S23	894	(rts same cts) and (request adj2 send) and (clear adj2 send)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 09:43
S24	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47

S25	1	S24 and (destination with address)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 09:45
S26	1	S24 and ("350" "351")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:40
S27	0	S24 and (crc error)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:20
S28	0	S24 and (crc same error same discard$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S29	847	(crc same error same discard$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S30	283	(crc with error with discard$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 10:21
S31	1	S24 and ("370" same "390")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:45
S32	1	S24 and (busy)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 11:21
S33	1	S24 and ("351")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 11:21
S34	1	10/036602	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:46
S35	1	S34 and snoop$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:46
S36	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47

S37	0	S36 and multicast$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 12:47
S38	0	S36 and (multicast$3 multi-cast$3 broadcast$3 broad-cast$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:15
S39	149	backpressure with threshold	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:27
S40	27	backpressure same threshold same overflow	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:35
S41	27	S40 and (packet cell)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 13:27
S42	7	(leaky leaky-bucket) same threshold same overflow	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 15:10
S43	106	(rts cts) same (multicast$3 multi-cast$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/10/24 15:10
S44	51	(rts cts) same (multicast$3 multi-cast$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 15:18
S45	1	"5875186".pn. and (multicast$3 with rts)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/24 15:19
S46	1	"20030043835".pn.	US-PGPUB; USPAT	OR	ON	2005/10/25 08:51
S47	1	S46 and (enable$3 same multi-sink)	US-PGPUB; USPAT	OR	ON	2005/10/25 08:54
S48	1	S46 and (enable$3)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:13
S49	1	S46 and (snoop$3)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:14
S50	240	(snoop$3) with (accept$3 reject$3)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:16
S51	96	S50 and (packet cell)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:15

S52	108	(snoop$3) with (accept$3 reject$3) with (packet cell data)	US-PGPUB; USPAT	OR	ON	2005/10/25 09:16
S53	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 09:42
S54	1	S53 and ("351" same ("370" "390"))	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:53
S55	387381	"1" and (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:54
S56	1	"6728206".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:54
S57	1	S56 and (bus)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:55
S58	1	S56 and (bus same "302")	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 11:55

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L1	14	("20030126233" "20010038633" "4769810" "6144636" "6212165" "6327246" "6460088" "6470016" "6480911" "6614758" "6625157" "6697362" "6700899" "6748435").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:09
L2	7	1 and (ring loop)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:10
L3	3	1 and (snoop$3)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/03 14:10